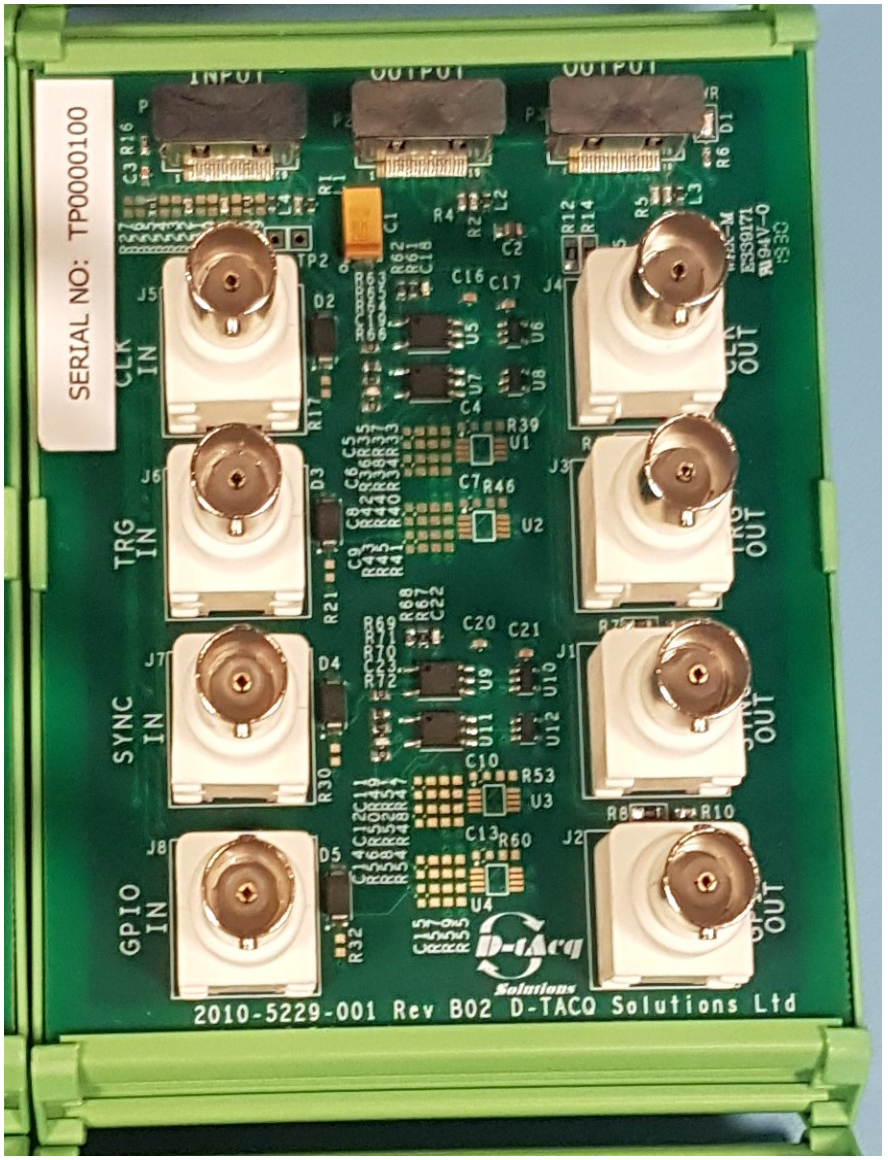
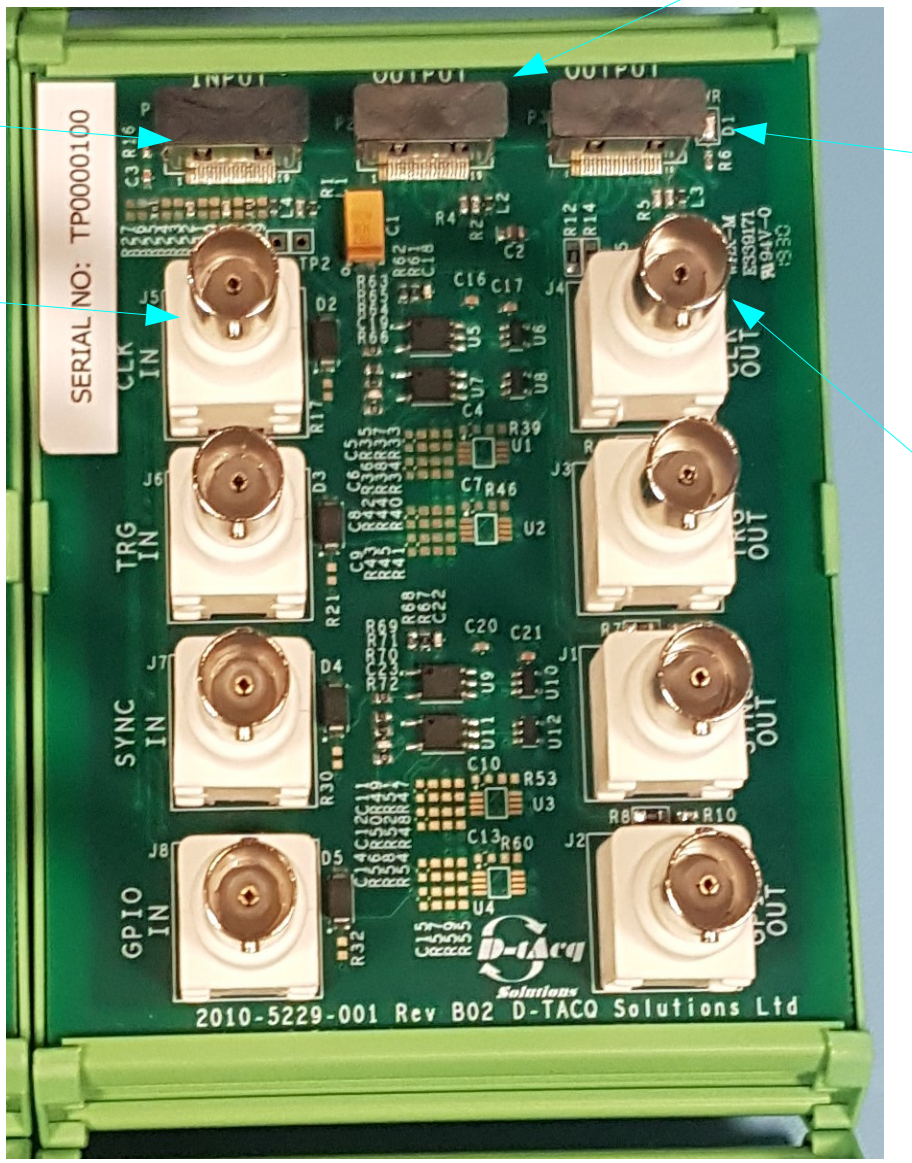
TERM10 User guide



Wiring diagram

Connect to carrier HDMI INPUT connector

Connect desired clock on CLK IN



Connect to carrier HDMI OUT connector

Optional. Use as a master output if using slave systems. Connect to slave HDMI IN.

Modified clock output visible on CLK OUT

Additions to sync_role

A new option to sync_role has been added to work alongside the term10 – rpmaster. This allows the system to be a master system, clocked from the rear panel, rather than from the front or from the internal clock.

The command can be used as such:

```
acq1001_349> set.site 0 sync_role rpmaster 200k
```

or

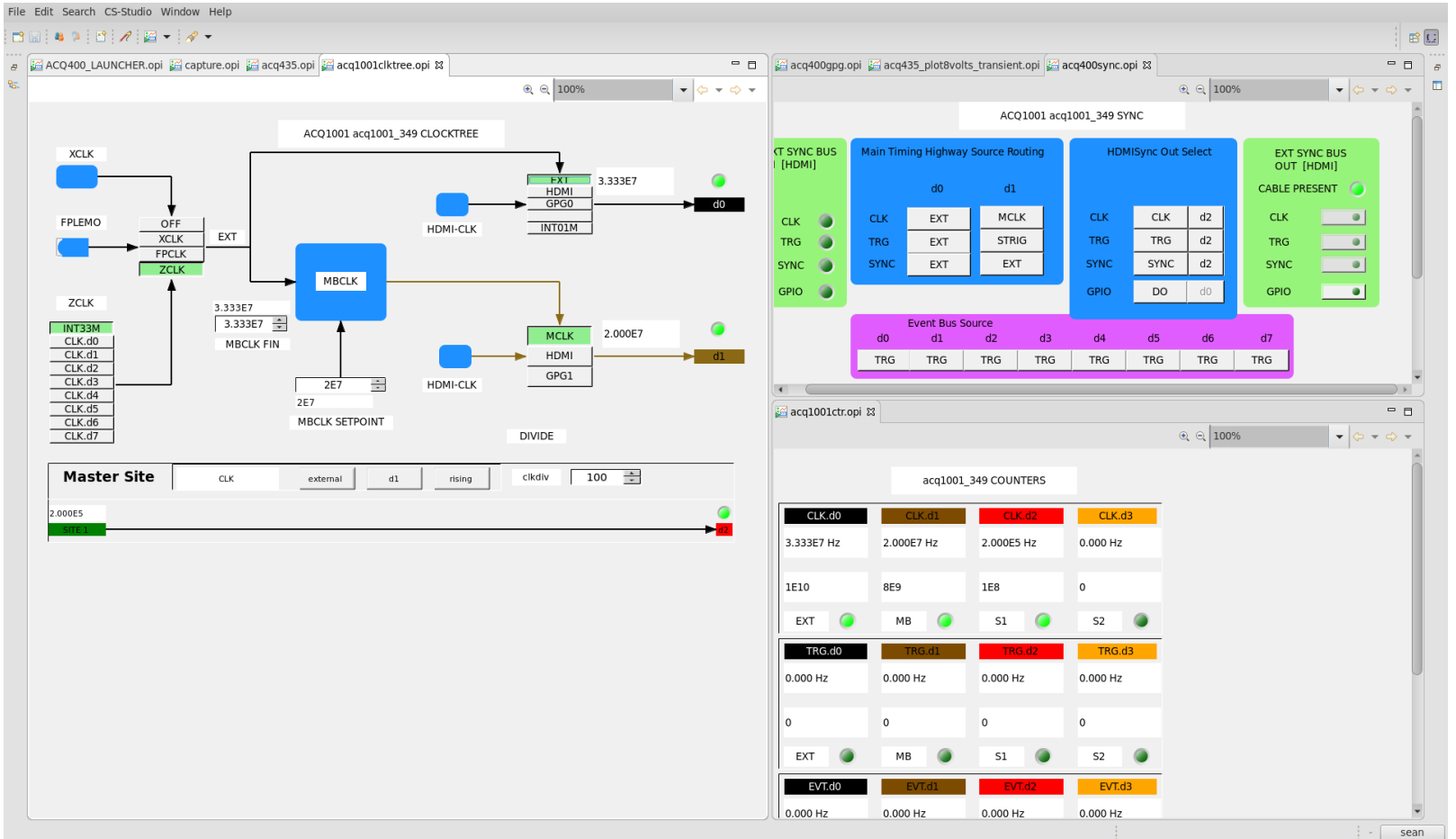
```
acq1001_349> set.site 0 sync_role rpmaster 2M
```

Before using rpmaster command

Here is the configuration of the system before using the rpmaster command. Note – this system has a

```
set.site 0 sync_role master 200000
```

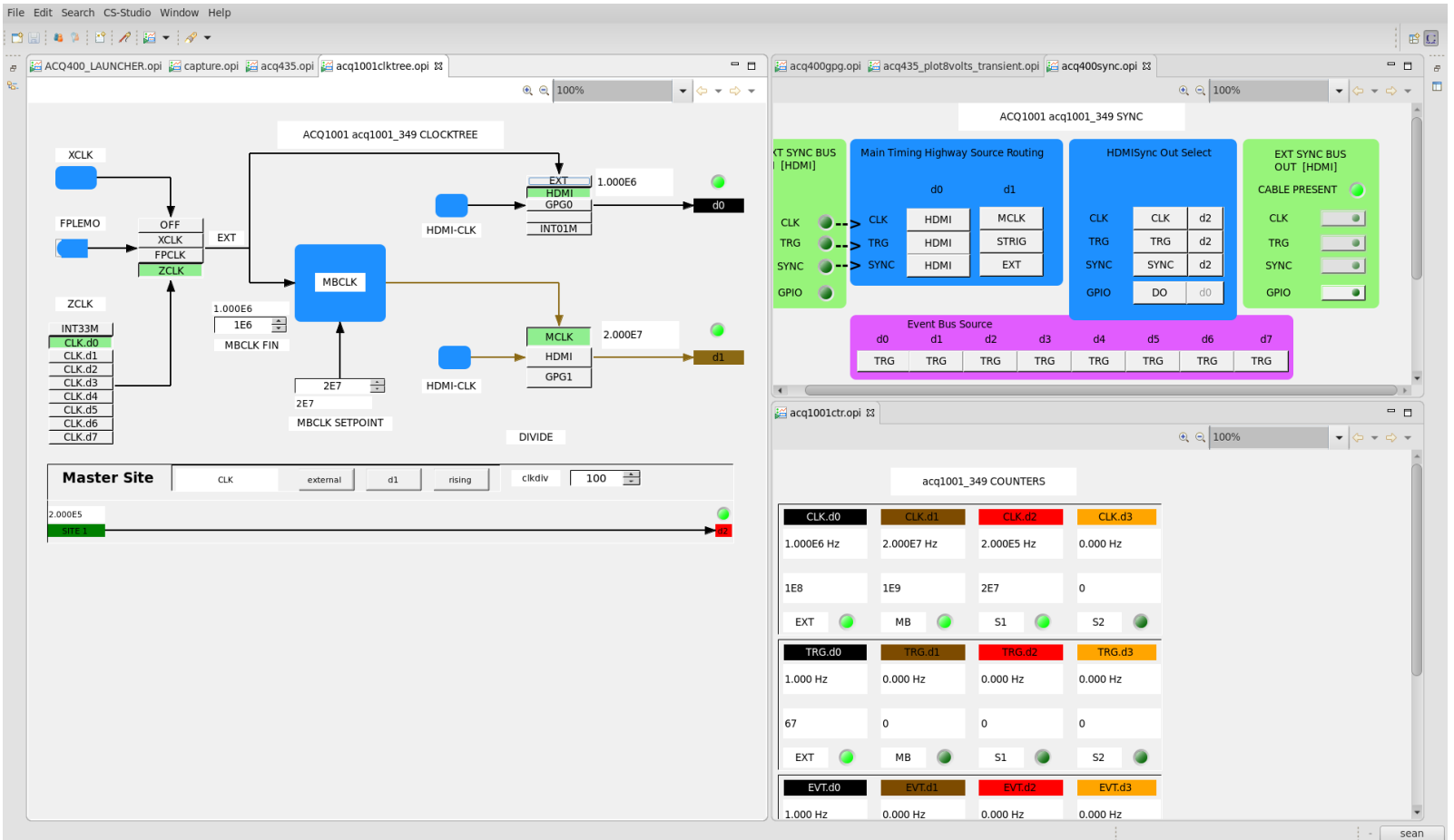
command in rc.user.



Using rpmaster command

Here is the configuration of the same system immediately after using:

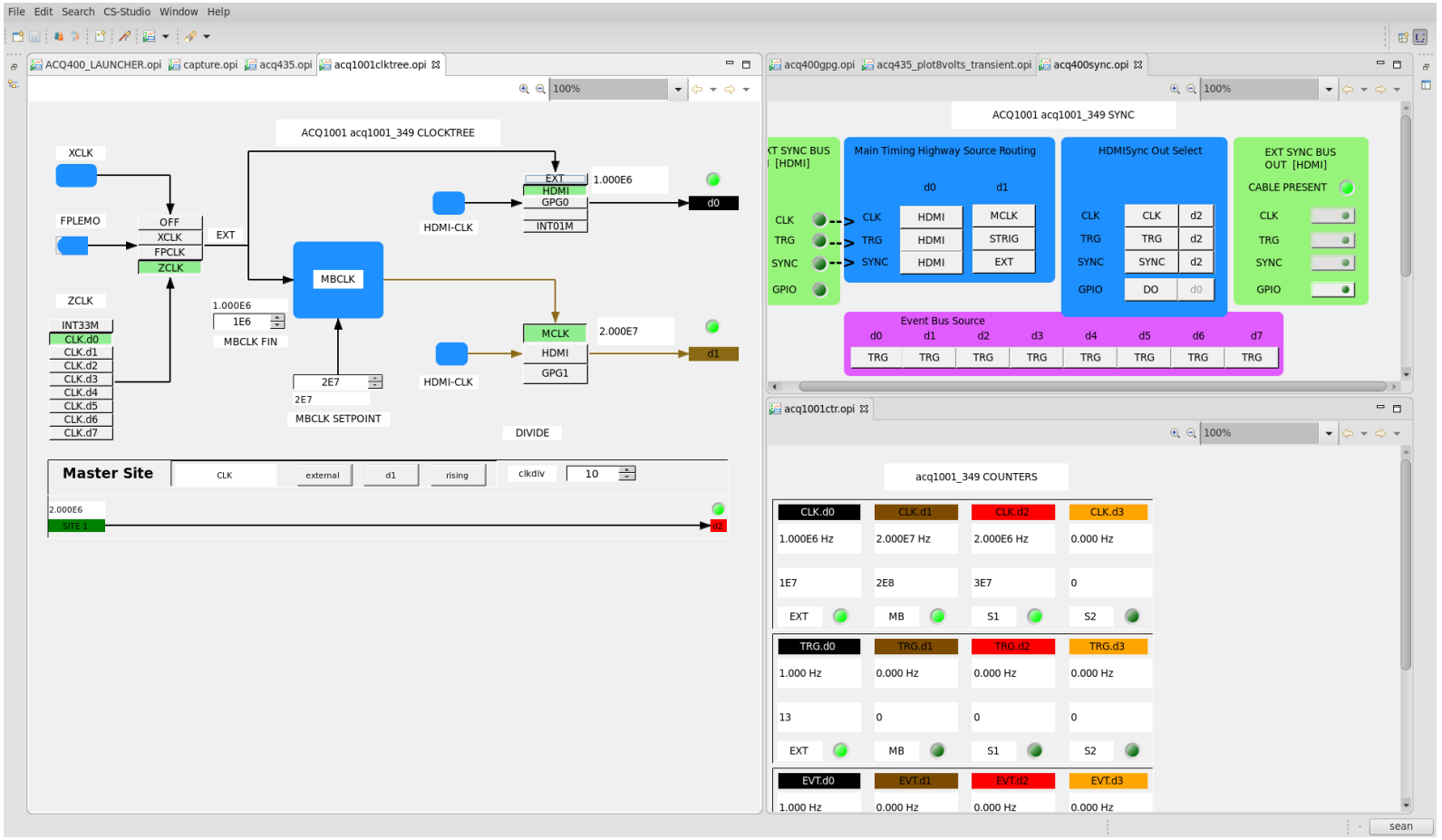
`set.site 0 sync_role rpmaster 200k`



Example rpmaster commands

sync_role can now take any clock parameter passed to it. For example (and demonstration purposes only as acq423 max clock speed is 200k) here is rpmaster with an argument of 2M.

```
set.site 0 sync_role rpmaster 2M
```



Scope trace of input and output from TERM10.

Here is the scope trace of the signal generator clock input on C3 (orange trace) and the d0 (EXT clock) output from the TERM10 on C4 (purple trace). The phase difference is approximately 80ns.

Please note that the ‘rpmaster’ command does NOT set this routing. To check this you will have to manually set the clock output selection to d0.

Scope trace of input and output from TERM10.

Here is the scope trace of the signal generator clock input on C3 (orange trace) and the d1 (MBCLK) output from the TERM10 on C4 (purple trace).